

IN THE SPECIFICATION:

Replace the paragraph 0049 on page 21 with the following:

Next, constraint K3 is propagated, with the result that the 128 Mbyte Memory is eliminated. This elimination occurs because both the 30 Gbyte and the 40 Gbyte DiskDrives have been eliminated. Thus, a join corresponding to a conjunction can be derived from the given constraints. This join could be stated logically: if both of the DC2 DiskController and the 700 MHz CPU are selected, then the 128 Mbyte Memory cannot be selected. To propagate this join, the bounceback detection process logically ORs the bounceback detection bit vector for the 30 Gbyte (eliminated by selection of the DC2 DiskController) and 40 Gbyte (eliminated by selection of the 700 MHz CPU) DiskDrives thereby yielding a bounceback detection bit vector of #0101 for the 128 Mbyte Memory. The elimination flag associated with the 128 Mbyte Memory is also set.

IN THE CLAIMS:

Replace the same-numbered pending claims with the following:

- 1 4. (Amended) The method of claim 1, wherein the method is implemented by a set of software instructions running on a computer.

- 1 20. (Amended) The method of claim 9, wherein the method is implemented by a set of software instructions running on a computer.